

Figure 2a
Prior Art

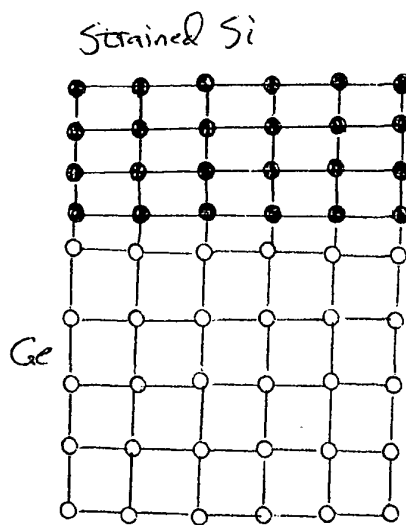


Figure 2b
Prior Art

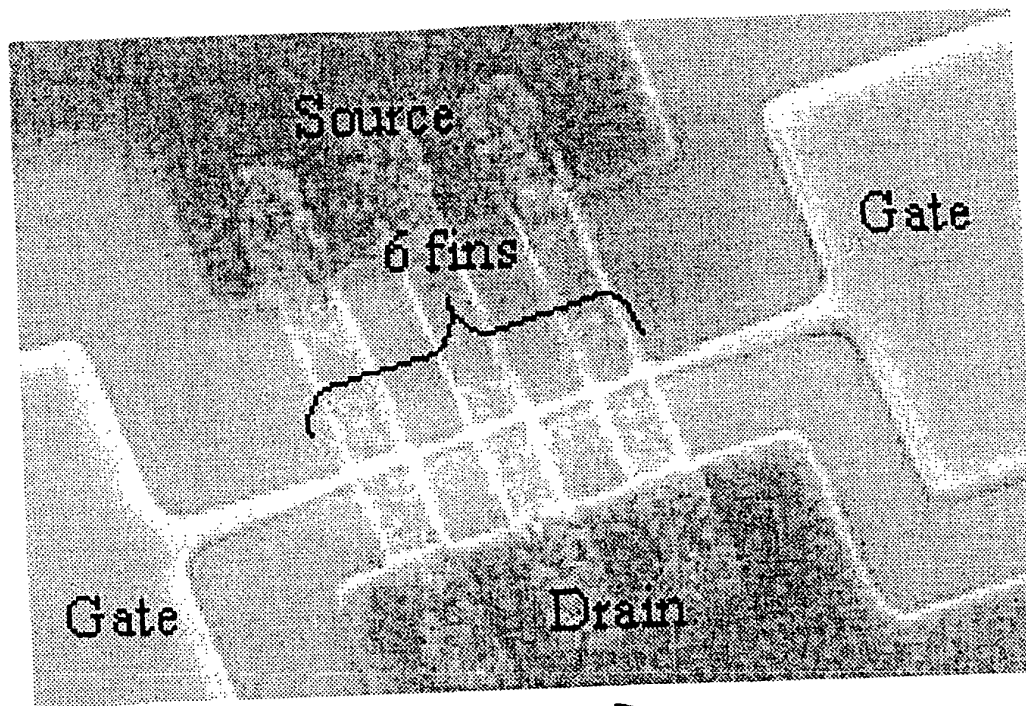
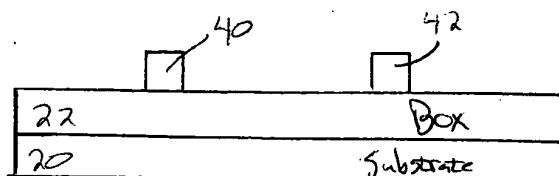


Figure 1 - Prior Art

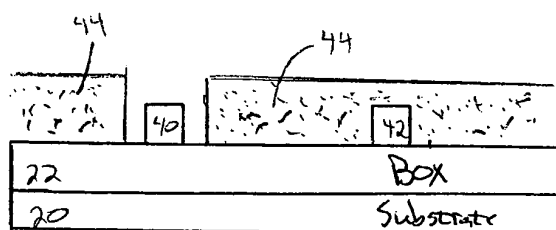
Figure 5a

38	Silicon
22	Buried oxide layer (BOX)
20	Substrate

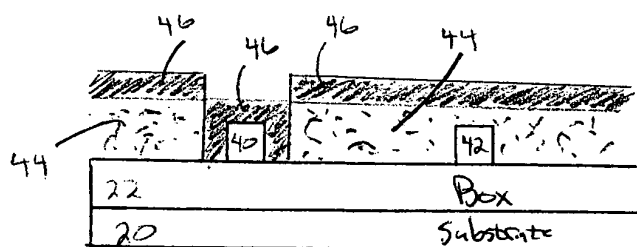
5b



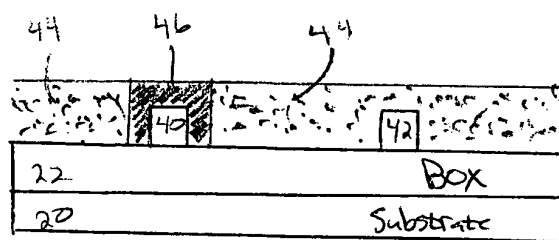
5c



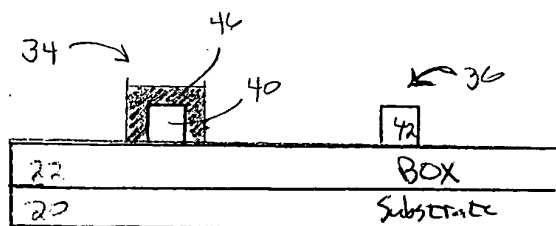
5d



5e



5f



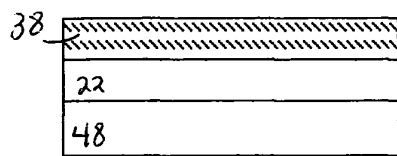


Figure 6a

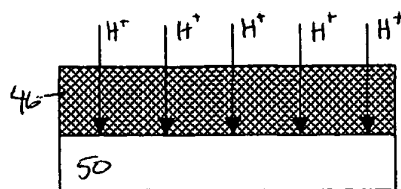


Figure 6c

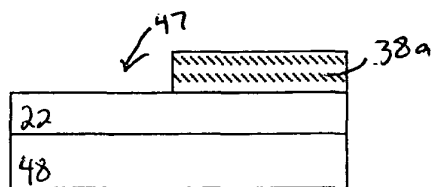


Figure 6b

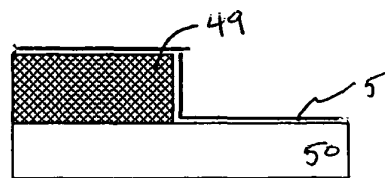


Figure 6d

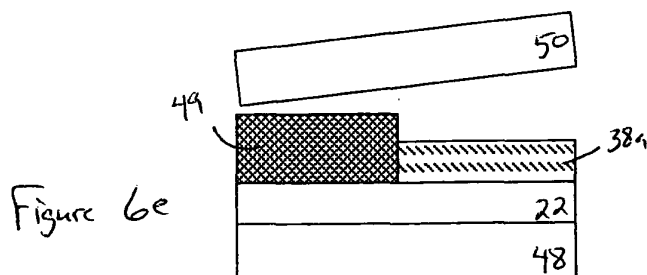


Figure 6e

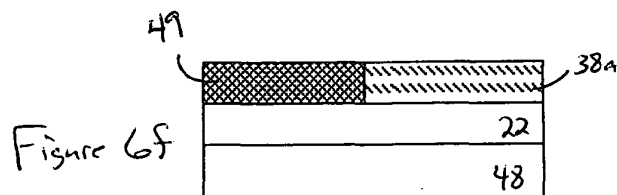


Figure 6f

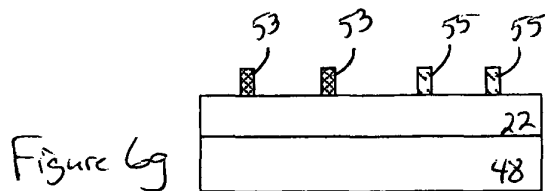


Figure 6g

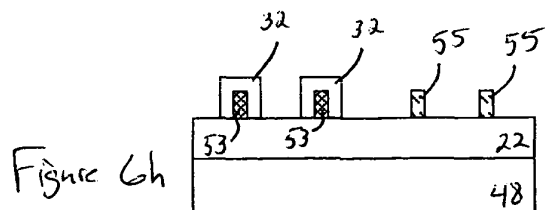


Figure 6h

Figure 7a

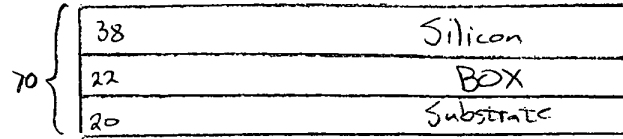


Figure 7b

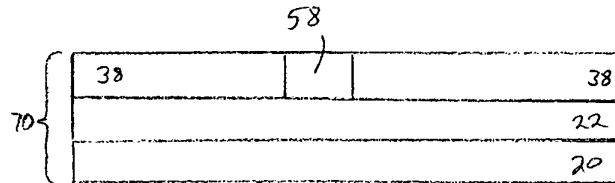


Figure 7c

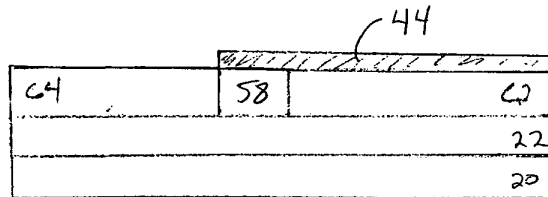


Figure 7d

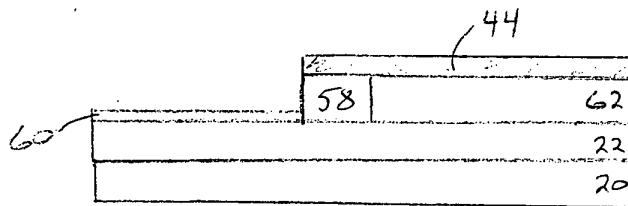


Figure 7e

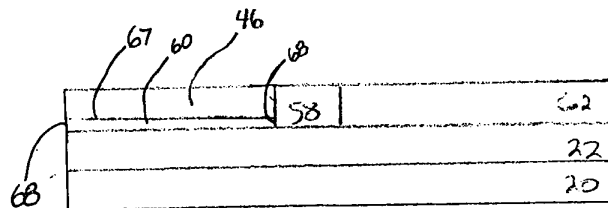


FIGURE 7f

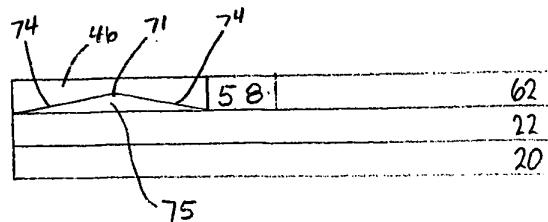


Fig. 8A

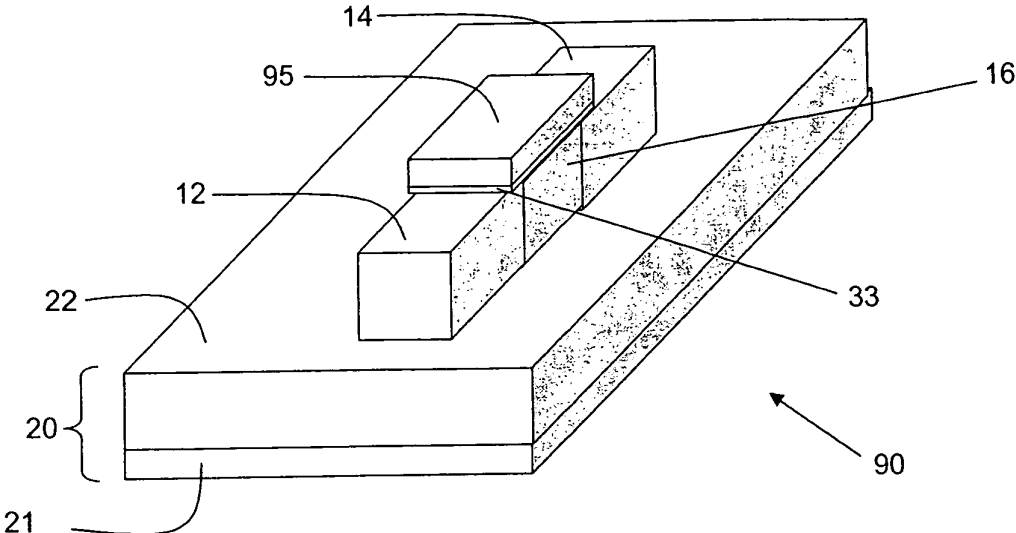


Fig. 8B

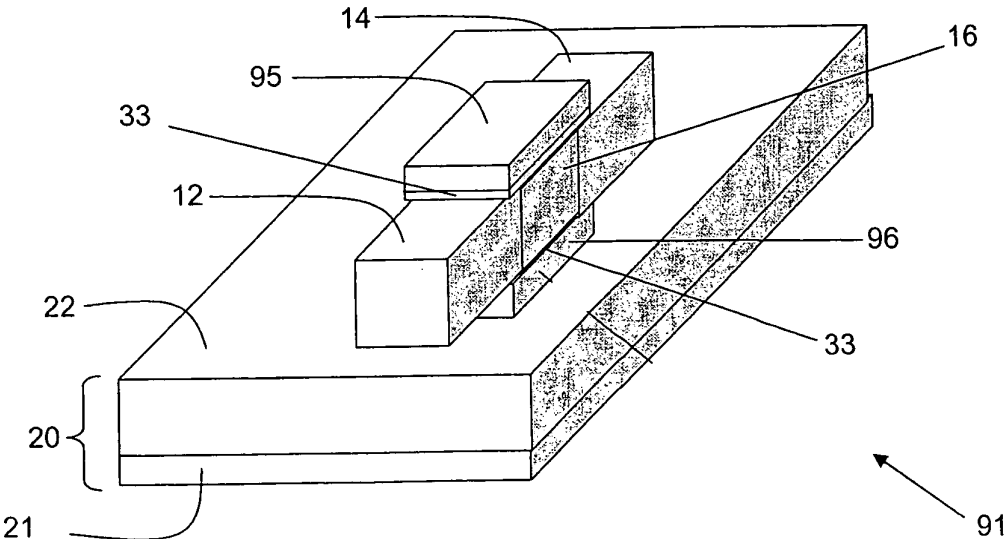


Fig. 8C

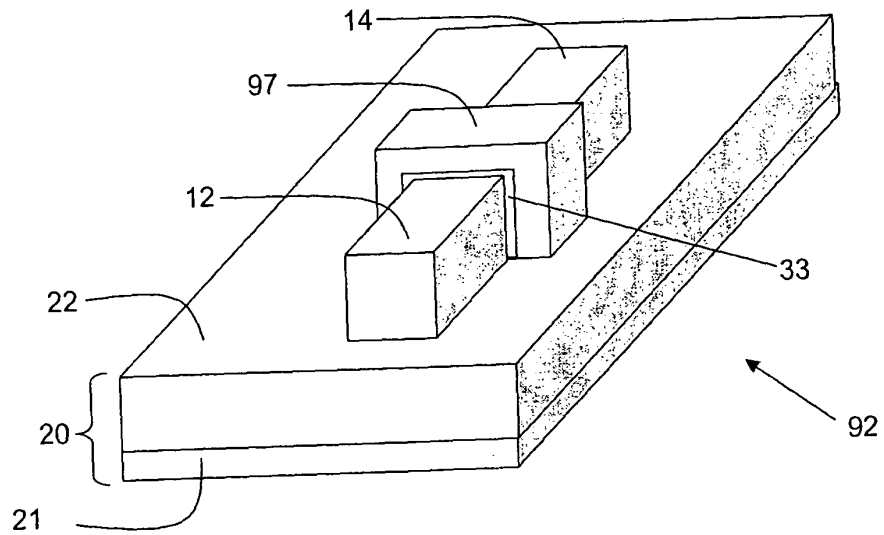


Fig. 8D

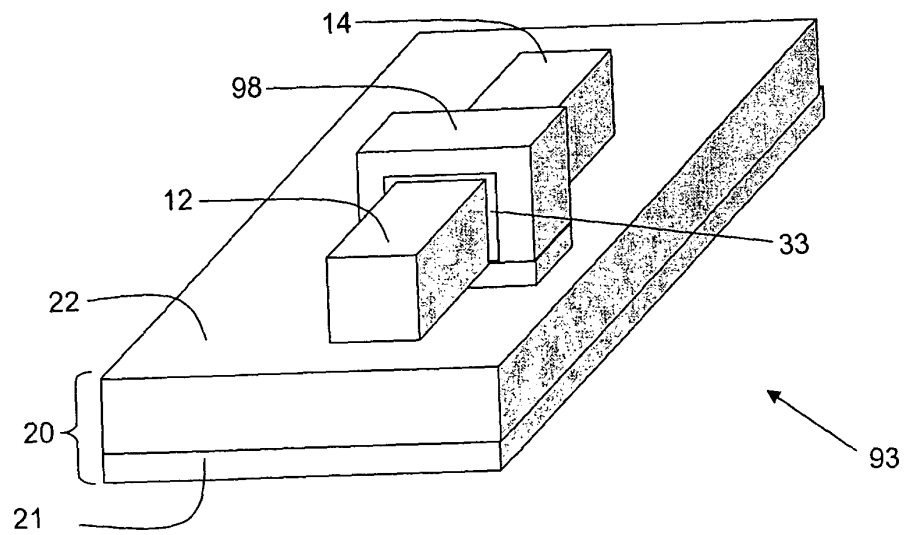
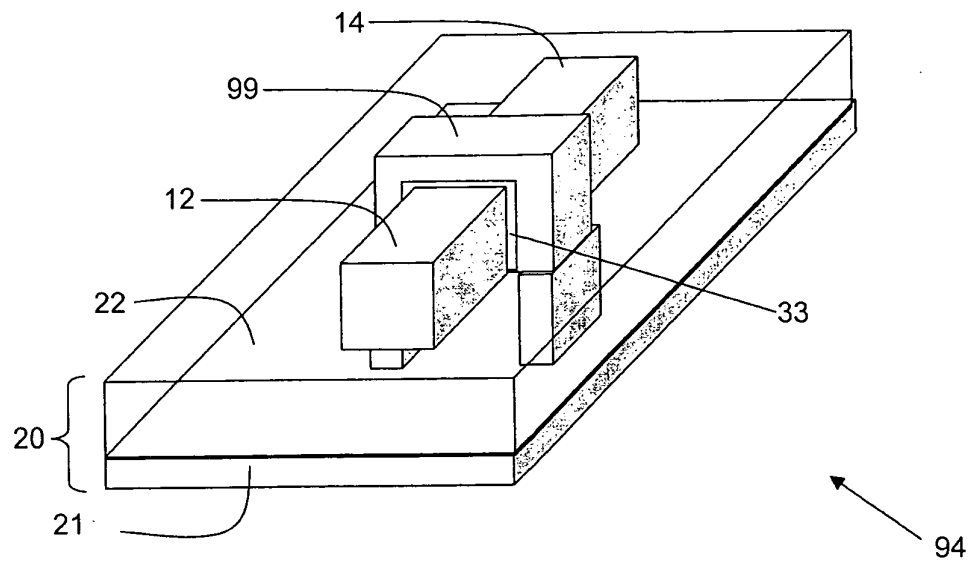


Fig. 8E



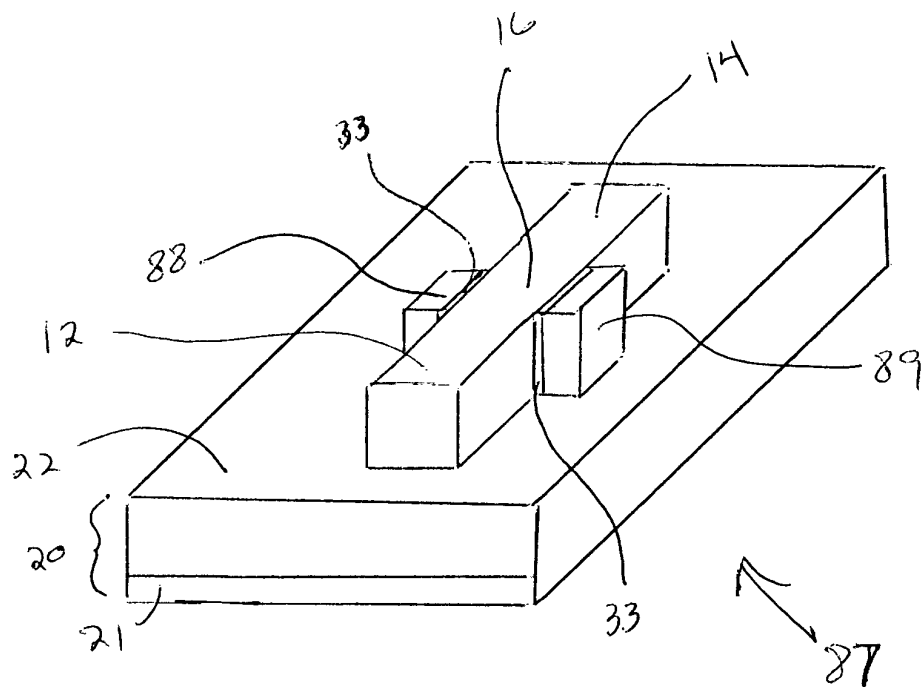


Figure 8F

Fig. 9

